

NOTES: UNLESS OTHERWISE SPECIFIED.

1. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, $\pm 5\%$.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL 24 PIN PARTS USED IN SOCKETS XU50 THRU XU53 SHOULD BE INSERTED WITH PIN 1 OF THE 24 PIN I.C. IN PIN 3 OF THE 25 PIN SOCKET. TO PREVENT IMPROPER INSERTION WHEN 24 PIN PARTS ARE USED, ITEM 79 (PLUG) SHOULD BE INSERTED INTO PINS 27 AND 28.
4. ALL JUMPERS ARE FACTORY INSTALLED TO MEET 80/20-4 HARDWARE COMPATIBILITY.
5. U3 THRU U6, U8 THRU U11, U50 - U53, C7 THRU C10, C12 THRU C15, AND C17 THRU C20 ARE SHOWN FOR CLARITY. ACTUAL COMPONENTS ARE CUSTOMER INSTALLED.
6. CDD PINS CN J1 AND J2 ARE ALL GROUNDED.
7. IMPROPERLY INSTALLED CONNECTORS (J1, J2 AND J3) COULD RESULT IN DAMAGE WHEN JUMPERS E1-E2, E3-E4, E5-E6, E7-E8, OR F9-F10 ARE INSTALLED.

741C4	U2B	1	RPS		
74LS04	U57	1	LSN		
74LS05	U58	2	LSN		
74LS04	U59	1	LSN	mm	137
74LS32	U59	1	LSN	mm	136
5.6K	R200-10		LSN	mm	137
1K	R21-10		LSN	mm	137
	R22-10		LSN	mm	137
	R23-10		LSN	mm	137
	R24-10		LSN	mm	137
	R25-10		LSN	mm	137
	R26-10		LSN	mm	137
	R27-10		LSN	mm	137
	R28-10		LSN	mm	137
	R29-10		LSN	mm	137
	R30-10		LSN	mm	137
	R31-10		LSN	mm	137
	R32-10		LSN	mm	137
	R33-10		LSN	mm	137
	R34-10		LSN	mm	137
	R35-10		LSN	mm	137
	R36-10		LSN	mm	137
	R37-10		LSN	mm	137
	R38-10		LSN	mm	137
	R39-10		LSN	mm	137
	R40-10		LSN	mm	137
	R41-10		LSN	mm	137
	R42-10		LSN	mm	137
	R43-10		LSN	mm	137
	R44-10		LSN	mm	137
	R45-10		LSN	mm	137
	R46-10		LSN	mm	137
	R47-10		LSN	mm	137
	R48-10		LSN	mm	137
	R49-10		LSN	mm	137
	R50-10		LSN	mm	137
	R51-10		LSN	mm	137
	R52-10		LSN	mm	137
	R53-10		LSN	mm	137
	R54-10		LSN	mm	137
	R55-10		LSN	mm	137
	R56-10		LSN	mm	137
	R57-10		LSN	mm	137
	R58-10		LSN	mm	137
	R59-10		LSN	mm	137
	R60-10		LSN	mm	137
	R61-10		LSN	mm	137
	R62-10		LSN	mm	137
	R63-10		LSN	mm	137
	R64-10		LSN	mm	137
	R65-10		LSN	mm	137
	R66-10		LSN	mm	137
	R67-10		LSN	mm	137
	R68-10		LSN	mm	137
	R69-10		LSN	mm	137
	R70-10		LSN	mm	137
	R71-10		LSN	mm	137
	R72-10		LSN	mm	137
	R73-10		LSN	mm	137
	R74-10		LSN	mm	137
	R75-10		LSN	mm	137
	R76-10		LSN	mm	137
	R77-10		LSN	mm	137
	R78-10		LSN	mm	137
	R79-10		LSN	mm	137
	R80-10		LSN	mm	137
	R81-10		LSN	mm	137
	R82-10		LSN	mm	137
	R83-10		LSN	mm	137
	R84-10		LSN	mm	137
	R85-10		LSN	mm	137
	R86-10		LSN	mm	137
	R87-10		LSN	mm	137
	R88-10		LSN	mm	137
	R89-10		LSN	mm	137
	R90-10		LSN	mm	137
	R91-10		LSN	mm	137
	R92-10		LSN	mm	137
	R93-1		LSN	mm	137

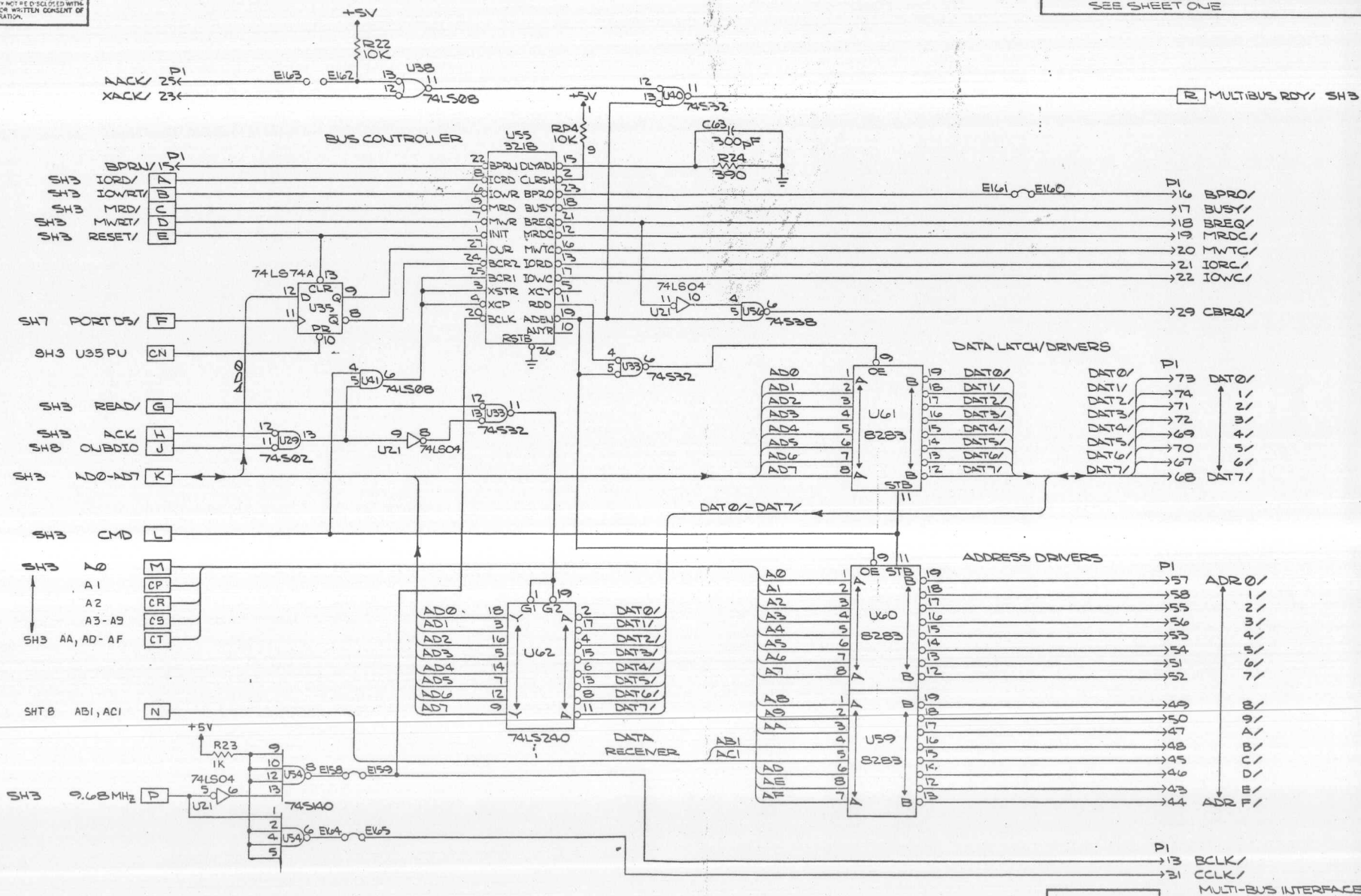
POWER AND GROUND LOCATOR CHART		POWER SNS					
REF DES	DEVICE TYPE	GND	+5V	+12V	-5V	-12V	AUX+5
U21	74LS04	7	14				
XU8-11		7	14				
XU3-6		7	14				
U41,68	74LS240	8	16				
U53,62	74LS240	10	20				
U23	74LS173	7	14				
U56	74LS238	7	14				
U33,40,42	74LS22	7	14				
U12,14	1483	7	14				
U13	1423	7		14		1	
U34,39	74LS27	7	14				
U45	74LS12	8	16				
U46	74LS13	8	16				
U37	74LS10	7	14				
U31	74LS200	7	14				
U54	74LS140	7	14				
U15,33	74LS74A	7	14				
U38,41	74LS20	7	14				
U30	74LS20	7	14				
U29	74LS02	7	14				
U57,13	74LS04	7	14				
U48	74LS28	8	16				
U27	5602	8	16				
U24	8224	8	16				
U32	8203	8	16				
U19	8253	12	24				
U16,18	8255A	7	26				
U22,49	8286	10	20				
U2,7	8287	10	20				
U17	8259A	4	28				
U20	8251A	4	28				
U44	LS238-1	9	25				
U35	3218	12	25				
U36	BC258W2	20	40				
U59-61	8253	10	20				
X44-47	8183-2	9					1B
U125,26	56ARE	7B	16				
U23	74C4	7					

[illegible]

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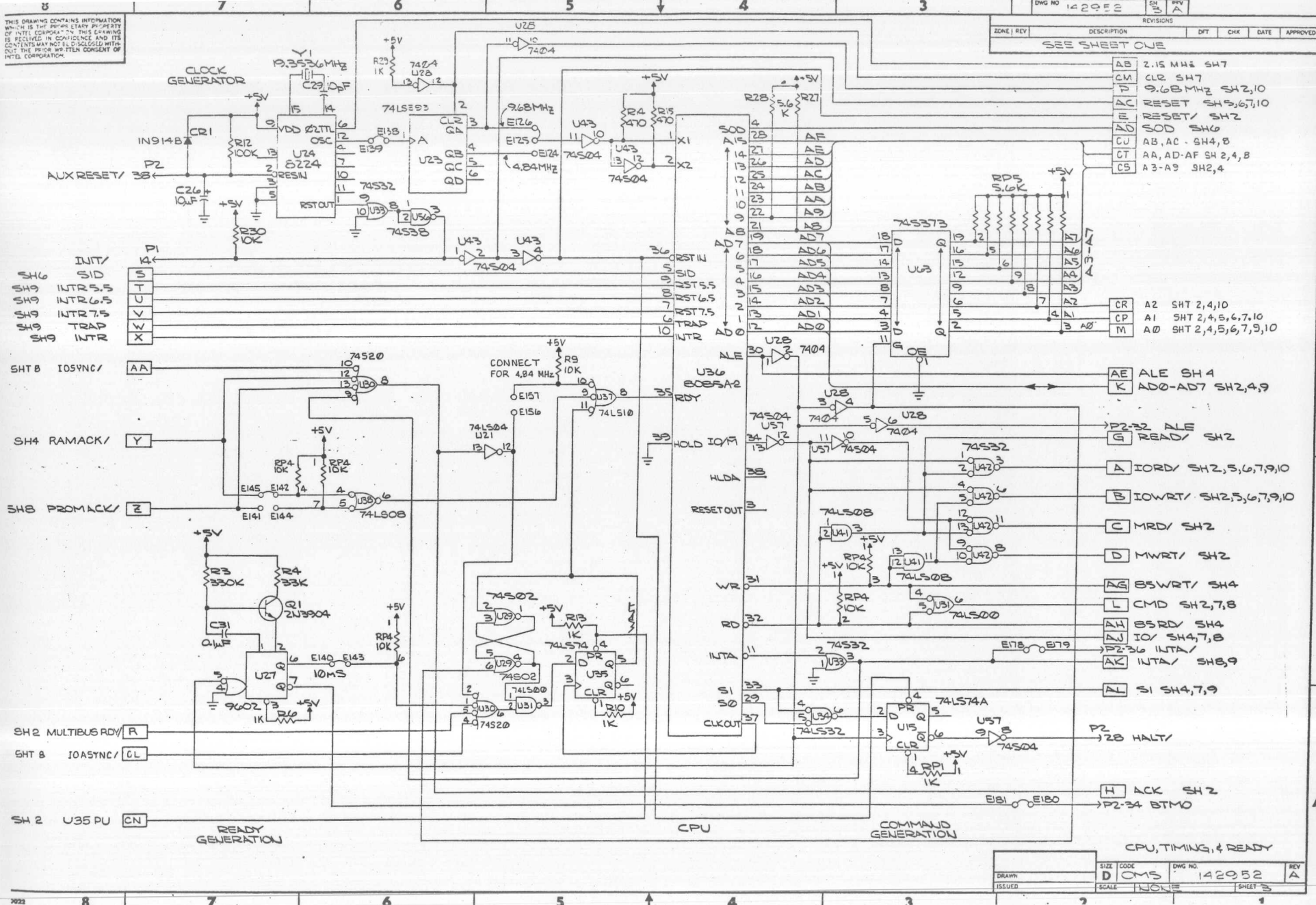
DWG NO 142952 SH 2 REV A

REVISIONS
ZONE REV DESCRIPTION DFT CHK DATE APPROVED
SEE SHEET ONE



PI 13 BCLK/ 31 CCLK/ MULTI-BUS INTERFACE
DRAWN D CM5 DWG NO 142952 REV A
ISSUED SCALE 1:1 SHEET 2

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		REVISE		3	A				
REVISIONS									
ZONE	REV	DESCRIPTION			DFT	CHK	DATE	APPROVED	
SEE SHEET ONE									

AB	2.15 MHz SH7
CM	CLR SH7
F	9.68 MHz SH2,10
AC	RESET SH3,6,7,10
E	RESET/ SH2
AD	SOD SH6
CU	AB, AC - SH4,8
CT	AA, AD-AF SH2,4,8
CS	A3-A9 SH2,4

CR	A2 SHT 2,4,10
CP	A1 SHT 2,4,5,6,7,10
M	A0 SHT 2,4,5,6,7,9,10

AE	ALE SH4
K	ADD-AD7 SH2,4,9

P2-32 ALE	READY SH2
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A	IORD/ SH2,5,6,7,9,10
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B	IOWRT/ SH2,5,6,7,9,10
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C	MRD/ SH2
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D	MWRT/ SH2
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AG	BSWRT/ SH4
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L	CMD SH2,7,8
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AH	BSRD/ SH4
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AJ	IO/ SH4,7,8
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P2-36 INTA/	AK
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AL	SI SH4,7,9
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P2-38 HALT/	
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H	ACK SH2
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P2-34 BTMO	
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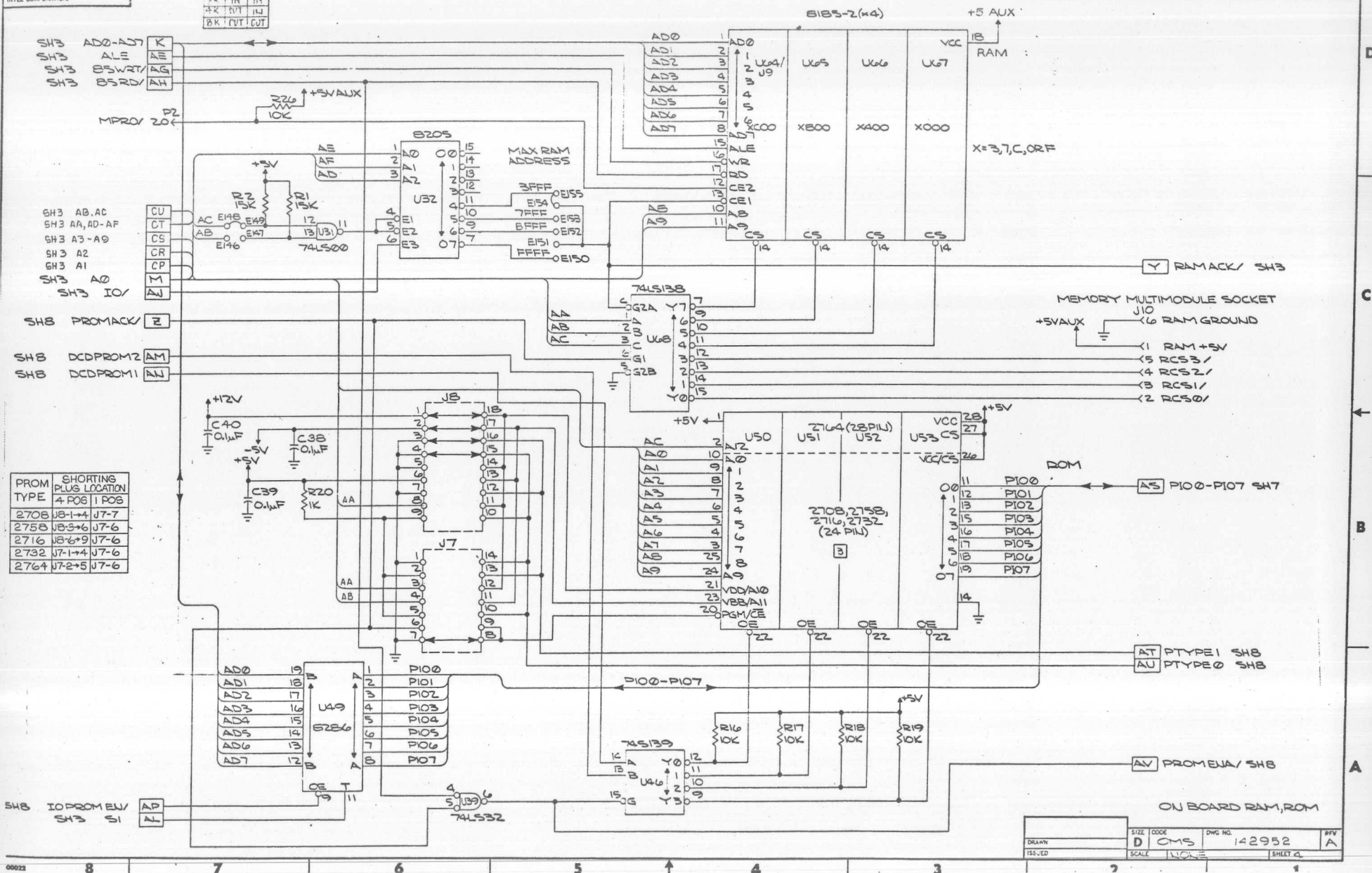
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CPU, TIMING, & READY

DRAWN	SIZE	CODE	DWG NO.	REV
ISSUED	SCALE	UOK	142052	A

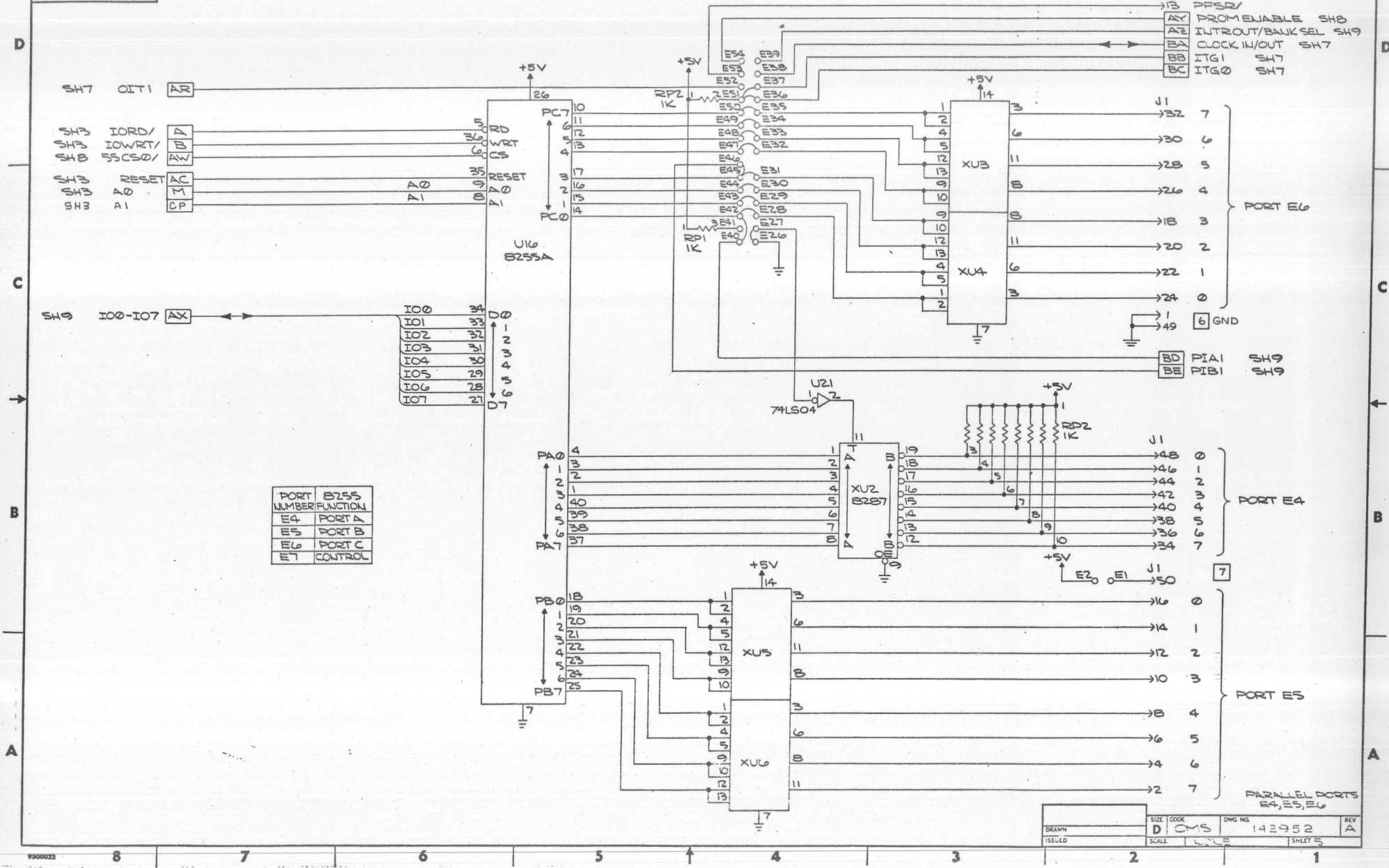
MEN: JUMPERS		
SIZE	43	44
2X	IN	IN
4X	OUT	IN
6X	OUT	OUT

PROM TYPE	SHORTING PLUG LOCATION	
	4 POS	1 POS
2708	J8-1→4	J7-7
2758	J8-3→6	J7-6
2716	J8-6→9	J7-6
2732	J7-1→4	J7-6
2764	J7-2→5	J7-6



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SEE SHEET ONE



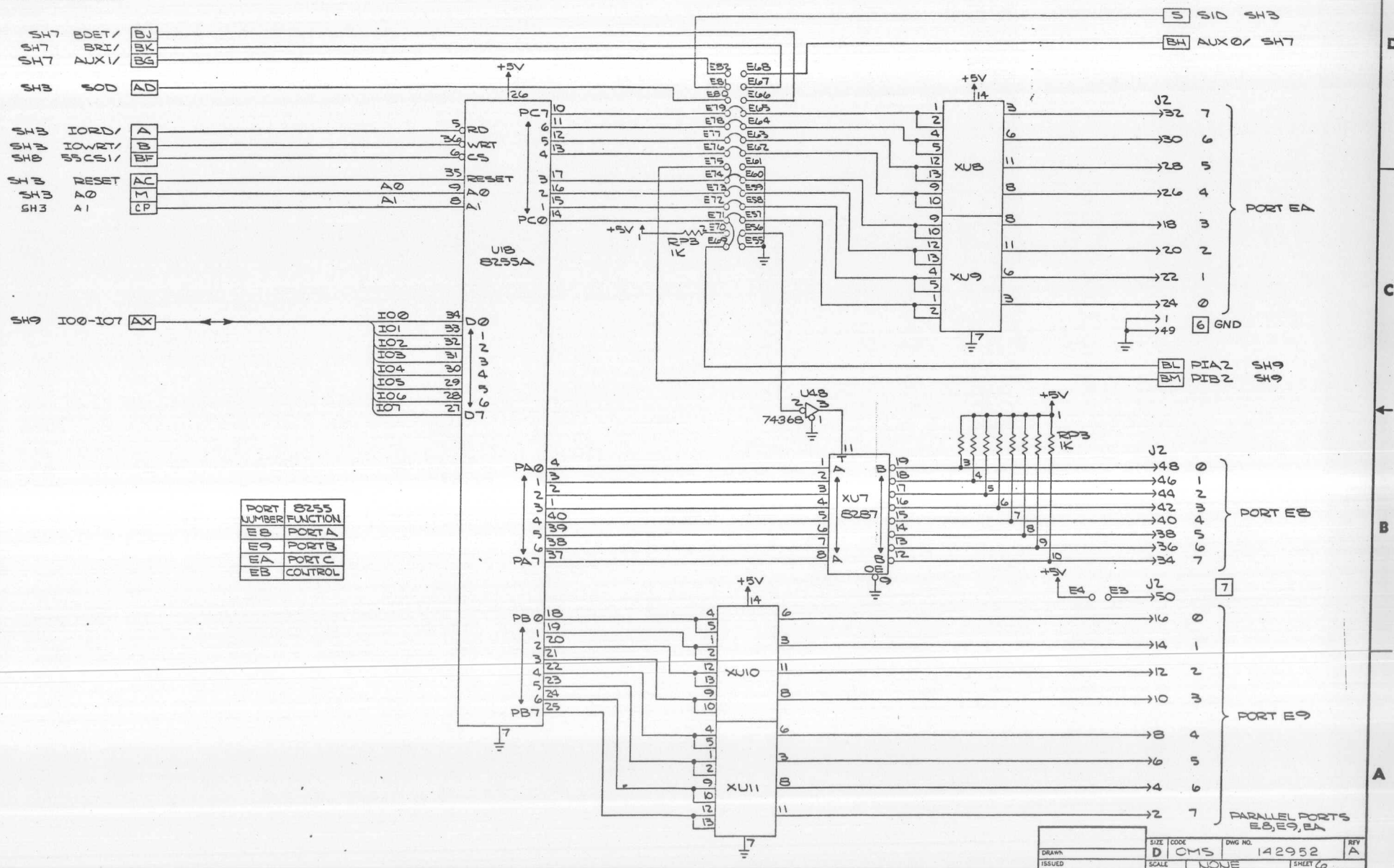
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DWG NO. 142952

REV. A

ZONE | REV | DESCRIPTION | DFT | CHK | DATE | APPROVED

SEE SHEET ONE

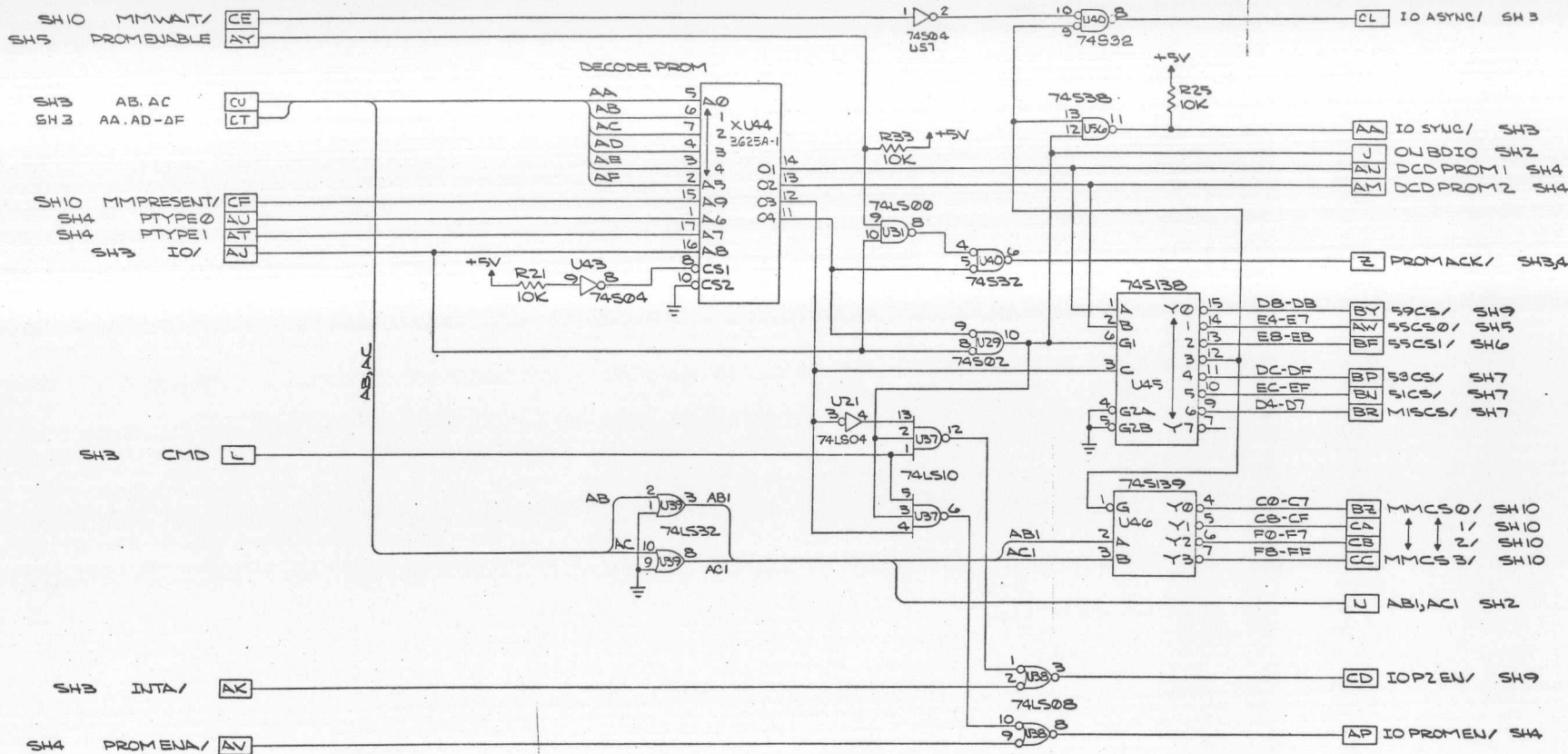


SERIAL PORT & TIMERS

DRAWN	SIZE	CODE	DWG. NO.	REV
	D	OMS	142952	A
ISSUED	SCALE		1/4" = 1"	SHEET 7

THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF INTEL CORPORATION. THIS DRAWING IS REVEALED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT THE PRIOR WRITTEN CONSENT OF INTEL CORPORATION.

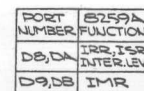
DRAWING NO.		REV		DATE	
142952		A		12/82	
ZONE / REV		DESCRIPTION		DATE	
		SEE SHEET ONE			



DECODE PROM & CHIP SELECT DECODE

DRAWN		SIZE		CODE		DRAWING NO.		REV	
ISSUED		D		OMS		142952		A	
SCALE		SCALE		SCALE		SCALE		SCALE	
NONE		NONE		NONE		NONE		NONE	

OIT 1
OIT 0



INTERRUPT CONTROLLER

	SIZE	CODE	DWG NO	REV
DRAWN	D	OMS	142932	A
ISSUED	SCALE	1:100	SHEET 9	

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DWG NO. 142952 SH 0 REV A

ZONE / REV DESCRIPTION DFT CHR DATE APPROVED
SEE SHEET ONE

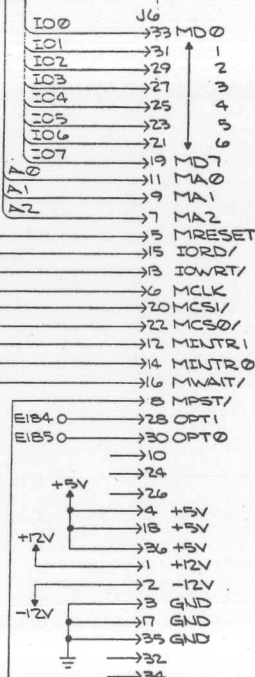
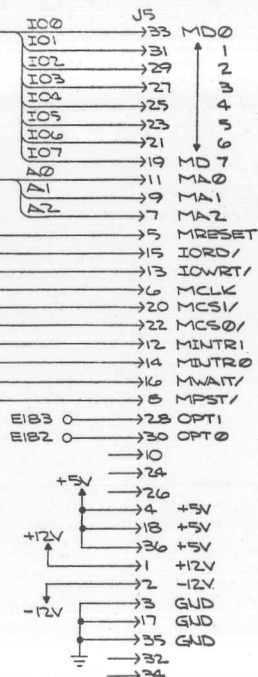
SH9 100-107 AX

SH3 A2 CR
SH3 A1 CP
SH3 A0, A1, A2 M

SH3 RESET AC
SH3 IORD/ A
SH3 IOWRT/ B
SH3 9.6MHz P
SH3 MMCS1/ CA
SH3 MMCS0/ BE

SH3 MMCS3/ CC
SH3 MMCS2/ CB

PORT NUMBER	MM CONN.
F0-F7	J6 CS0
F8-FF	J6 CS1
C0-C7	J5 CS0
C8-CF	J5 CS1



CE MMWAIT/ SH8

CF MMPRESENT/ SH8
CK MIOINT 2 SH9
CJ MIOINT 3 SH9
CH MIOINT 0 SH9
CG MIOINT 1 SH9

MULTIMODULE INTERFACES

SIZE	CODE	DWG NO.	REV
D	CMS	142952	A
SCALE	DATE	SHEET	NO
1:1	NOV 82	10	1